

ECE M.Tech project titles:

2015 Batch M. Tech Project titles (SSP)

S.No	Roll. No.	Name of the student	Project Title
1.	15761D4501	BHEEMALA KRANTHI SANDHYA	A SHARED MULTIPLIER FOR EFFECTUAL AREA OF 1024 POINT FAST FOURIER TRANSFORM PROCESSOR
2.	15761D4502	CHILUKURI SIVA NAGA MALLESWARY	BRAIN TUMOUR IDENTIFICATION USING NN CLASSIFICATION BY WAVELET TRANSFORM METHOD IN MRI IMAGES
3.	15761D4503	DASARI SIRISHA	DIABETIC RETINOPATHY USING MORPHOLOGICAL PROCESS AND HOUGH TRANSFORM
4.	15761D4504	GADE MEENAKSHI	SKIN LESION CLASSIFICATION OF MELONAMA USING ABCD PARAMETERS BY CONTOUR EDGE DETECTION
5.	15761D4505	GOGULAMUDI NAGENDRA BABU	AWARE REALY BASED SPECTRUM ACCESSING FOR PRIMARY USERS IN COGNITIVE RADIO OFDM BASED NETWORKS SYSTEM WITH POWER EQUALIZATION
6.	15761D4506	KAPULURU CHENCHIAIAH	MONITORING PRICE TAG WITH EPAPER BY USING 6LoWPAN AND MQTT
7.	15761D4507	LINGALA JHANSI	DETECTION OF DISEASE USING EEG SIGNAL BASED ON PSO AND NN CLASSIFIER
8.	15761D4508	MALAMANTI VINOD KUMAR	SECURITY OF INTEGRATED CIRCUITS PHYSICAL DESIGN IN 45 NM TECHNOLOGY
9.	15761D4509	MANDAPATI ANIL KUMAR	DESIGN AND IMPLEMENTATION OF ZYNQ BASED RECONFIGURABLE SYSTEM FOR JPEG 2000 COMPRESSION.
10.	15761D4510	MANIMALA SINDHURA	BRAIN WAVE SENSOR BASED VEHICLE ATTENTION TRACKING USING ARDUINO
11.	15761D4511	NUTAKKI ANUSHA	DETECTION OF LOGO USING WAVELET TRANSFORM ANALYSIS AND NN CLASSIFIER
12.	15761D4512	RAVULA VENU GOPALARAO	INVISIBLE WATERMARKING TECHNOLOG USING SVD FOR COPY RIGHT SECURITY PROTECTION.
13.	15761D4513	SEELAM SHAMILI	FIXED KEYPOINT PRESERVING FOR FACE RECOGRATION UNDER NON-UNIFORM ILLUMINATION CONDITIONS
14.	15761D4514	SYED AFROZBANU	ANALYSIS AND IMPLEMENTATION OF FAULT TOLERANT PARALLEL FFTS & IFFTS USING PARITY SOS AND ECC
15.	15761D4515	YANDRATHI RAMYANKA	LUNG NODULAR PATTERN FOR INTERSTITIAL LUNG DISEASES USING NEURAL NETWORK CLASSIFICATION

2016 Batch M. Tech Project titles (VLSI & ES)

S.No	Roll. No.	Name of the student	Project Title
1.	16761D6801	A. MOUNIKA	IMPLEMENTATION AND CHARACTERIZATION OF MODULAR RESIDUE REVERSE CONVERTERS BY SELF SELECTION OF MODIFIED CARRU USING FPGA
2.	16761D6804	G. VAMSI KRISHNA	ADVANCED TESTING METHODS FOR REVERSIBLE LOGIC CIRCUITS
3.	16761D6805	G. NAGA LAKSHMI	DESIGN AND ANALYSIS OF PSO BASED LMPP AND GMPP FOR PV SYSTEMS UNDER PARTIAL SHADING CONDITIONS USING FPGA
4.	16761D6812	O. MOUNIKA	NEURAL NETWORK APPROACH FOR ENERGY EFFICIENCY IN CELLULAR SYSTEM
5.	16761D6815	S. VIDHYULLATHA	DESIGN AND IMPLEMENTATION OF ZYNQ SOC BASED EMBEDDED VISION SYSTEM FOR REAL-TIME APPLICATIONS

2017 Batch M. Tech Project titles (VLSI & ES)

S.No	Roll. No.	Name of the student	Project Title
1.	17761D6802	GANUGAPHATI VENKATA SAI MOHAN	EFFECTIVE USAGE OF CHIP AREA BY OPTIMIZING THE DEAD SPACE
2.	17761D6803	K. RAJASEKHARA REDDY	BLACK BOX MODEL BASED VLSI HIERARCHICAL FLOORPLANNING
3.	17761D6807	REPUDI VEERENDRA KUMAR	REDUCING THE DELAY BY OPTIMIZING THE VIA IN COMPACT AUTOMATIC METAL ROUTING ALGORITHM
4.	16761D6817	SINGAPAM RAJANI KUMARI	DESIGN OF EFFICIENT 32-BIT LOW POWERVARIABLE LATENCY PARALLEL PREFIX BRENT-KUNG ADDER
5.	17761D6810	Y. MADHAVI	FAULT FREE FFT PROCESSOR DESIGN USING PARSEVAL CHECKS

2018 Batch M. Tech Project titles (VLSI & ES)

S.No	Roll. No.	Name of the student	Project Title
1.	18761D6801	ASHA GANTA	DESIGN AND DEVELOPMENT OF ANTICIPATIVE GESTURE RECOGNITIO SYSTEM FOR REAL TIME APPLICATIONS
2.	18761D6802	BOGGULA LAKSHMI THIRUPATHAMMA	DESIGN OF LOW COMPLEX VLSI ARCHITECTURE OF IDENTITY BASED FULLY HOMOMORPHIC ENCRYPTION (IBFHE)
3.	18761D6803	KONDA BHANU	PHYSICAL MODELING OF SRAM CELL FOR SUB THRESHOLD HIGH STATIC NOISE MARGIN
4.	18761D6804	MULAGALAPALLI MOUNIKA	DESIGN AND DEVELOPMENT OF HUMAN RECOGNITION SYSTEM FOR ENERGY CONSUMPTION APPLICATIONS
5.	18761D6805	NUNNA SWATHI	DESIGN OF ADVANCED AND AUTOMATIC WATER LEVEL METER FOR ALTERING POPULATION ABOUT FLOODS
6.	18761D6805	VEMIREDDY KAVYA	DESIGN OF ENERGY EFFICIENT FEATURE MAP ENCODING SCHEME USING MLC STTRAM MEMORY MANAGEMINT

2019 Batch M. Tech Project titles (VLSI & ES)

S.No	Roll. No.	Name of the student	Project Title
1.	19761D6801	Akula Hema Sankara Subrahmanya Vara Prasad	DESIGN AND IMPLEMENTATION OF HIGH SPEED AND SECURABLE FFCKBA